

Binary dump of the 82S123A (74S188) PROM contents (Professional-DOS Release)

IC1: address	progr. Byte	6502 prozessor selected address space	Control function description
0	28	0x0xxx	
1	29		
2	28	0x8xxx	
3	29		
4	53	0x40xx..0x47xx	Enable internal RAM (range 0x0000 to 0x07ff) and simulate an external address of 0x48xx..0x4Fxx
5	53	0x48xx..0x4Fxx	Enable internal RAM (range 0x0800 to 0x0fff)
6	28	0xCxxx	
7	29		
8	28	0x2xxx	
9	29		
10	28	0xAxxx	Empty address space , because of the removed external default DOS ROM and missing enabling of the internal one
11	29		
12	13	0x60xx..0x67xx	Enable the internal ROM (range 0x0800 to 0x0fff) and simulate an external address of 0x68xx..0x6Fxx
13	141	0x68xx..0x6Fxx	Enable the internal ROM (range 0x0000 to 0x07ff) and store the values of the address lines A1 and A2 to the internal clock mode Flip-Flops
14	44	0xExxx	Enable the internal ROM (range 0x2000 to 0x2fff)
15	45		
16	52	0x10xx..0x17xx	Enable internal RAM (range 0x1000 to 0x17ff) as additional mirror from 0x5000
17	29	0x18xx..0x1Fxx	
18	28	0x9xxx	
19	29		
20	52	0x50xx..0x57xx	Enable internal RAM (range 0x1000 to 0x17ff)
21	52	0x58xx..0x5Fxx	Enable internal RAM (range 0x1800 to 0x1fff) and simulate an external address of 0x50xx..0x57xx
22	28	0xDxxx	
23	29		
24	28	0x3xxx	
25	29		
26	28	0xBxxx	Empty address space , because of the removed external default DOS ROM and missing enabling of the internal one
27	29		
28	10	0x70xx..0x77xx	Enable the internal ROM (range 0x1000 to 0x17ff), switch the address bits A[0..3] to the internal AL address bus, switch the internal 4-Bit register output to the internal AH address bus and store the current AL address bus value (A[0..3])
29	74	0x78xx..0x7Fxx	Enable the internal ROM (range 0x1800 to 0x1fff), simulate an external address of 0x70xx..0x77xx, switch the address bits A[4..7] to the internal AL address bus, switch the internal 4-Bit register output to the internal AH address bus and store the current AL address bus value (A[4..7])
30	44	0xFxxx	Enable the internal ROM (range 0x3000 to 0x3fff)
31	45		

Function oriented listing of the 82S123A (74S188) PROM contents (Professional-DOS Release)

Line label	6502 processor selected address space					6502 processor selected address space	programmed byte (dez)		O1&O8-Function	ROM-/OE			O4..O6-Function	/SELECT-A[0..3][4..7]->AL			O2-, O3-, O7-Function
	A15	A14	A13	A12	A11		O8	O1		O5	O6	O4		O7	O3	O2	
IC1-Addr	A1	A2	A3	A4	A0												
0	0	0	0	0	0	0x0xxx	28	0 0		1	0	1		0	1	0	
1	0	0	0	0	1		29	0 1		1	0	1		0	1	0	
16	0	0	0	1	0	0x10xx..0x17xx	52	0 0		1	1	0	RAM-Mirror (0x50xx..0x57xx)	0	1	0	
17	0	0	0	1	1	0x18xx..0x1Fxx	29	0 1		1	0	1		0	1	0	
8	0	0	1	0	0		28	0 0		1	0	1		0	1	0	
9	0	0	1	0	1	0x2xxx	29	0 1		1	0	1		0	1	0	
24	0	0	1	1	0		28	0 0		1	0	1		0	1	0	
25	0	0	1	1	1	0x3xxx	29	0 1		1	0	1		0	1	0	
4	0	1	0	0	0	0x40xx..0x47xx	53	0 1	A-EXT: 0x48xx..0x4Fxx	1	1	0		0	1	0	
5	0	1	0	0	1	0x48xx..0x4Fxx	53	0 1		1	1	0	Enable RAM	0	1	0	
20	0	1	0	1	0	0x50xx..0x57xx	52	0 0		1	1	0		0	1	0	
21	0	1	0	1	1	0x58xx..0x5Fxx	52	0 0	A-EXT: 0x50xx..0x57xx	1	1	0		0	1	0	
12	0	1	1	0	0	0x60xx..0x67xx	13	0 1	A-EXT: 0x68xx..0x6Fxx	0	0	1	Enable LO-ROM (DOS-Extension)	0	1	0	
13	0	1	1	0	1	0x68xx..0x6Fxx	141	1 1	Fetch-2MHz-Mode	0	0	1		0	1	0	
28	0	1	1	1	0	0x70xx..0x77xx	10	0 0		0	0	1	Enable LO-ROM	0	0	1	
29	0	1	1	1	1	0x78xx..0x7Fxx	74	0 0	A-EXT: 0x70xx..0x77xx	0	0	1	(GCR-Conversion-Tables)	1	0	1	
2	1	0	0	0	0		28	0 0		1	0	1		0	1	0	
3	1	0	0	0	1	0x8xxx	29	0 1		1	0	1		0	1	0	
18	1	0	0	1	0		28	0 0		1	0	1		0	1	0	
19	1	0	0	1	1	0x9xxx	29	0 1		1	0	1		0	1	0	
10	1	0	1	0	0		28	0 0		1	0	1		0	1	0	
11	1	0	1	0	1	0xAxxx	29	0 1		1	0	1	Empty address space (because of the removed default DOS ROM)	0	1	0	
26	1	0	1	1	0		28	0 0		1	0	1		0	1	0	
27	1	0	1	1	1	0xBxxx	29	0 1		1	0	1		0	1	0	
6	1	1	0	0	0		28	0 0		1	0	1		0	1	0	
7	1	1	0	0	1	0xCxxx	29	0 1		1	0	1		0	1	0	
22	1	1	0	1	0		28	0 0		1	0	1		0	1	0	
23	1	1	0	1	1	0xDxxx	29	0 1		1	0	1		0	1	0	
14	1	1	1	0	0		44	0 0		0	1	1		0	1	0	
15	1	1	1	0	1	0Exxx	45	0 1		0	1	1	Enable HI-ROM	0	1	0	
30	1	1	1	1	0		44	0 0		0	1	1	(Profess.-DOS)	0	1	0	
31	1	1	1	1	1	0xFxxx	45	0 1		0	1	1		0	1	0	